

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Chris J. Newburn, et al.

Application No.: 10/750,715

Filed: December 31, 2003

For: **Processor and Memory Controller Capable  
Of Use In Computing System That Employs  
Compressed Cache Lines' Worth Of  
Information**



)  
Examiner: Hashem Farrokh  
)  
Art Unit: 2186  
)  
)  
)  
)  
)

Commissioner of Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

06/08/2006 SSITHIB1 00000010 10750715

03 FC:1201

200.00 OP  
50.00 OP

04 FC:1202

*I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on*

6/5/06

(Date of Deposit)

Jenny Kim

(Printed name)

(Signature)

(Date)

AMENDMENT AND RESPONSE

Dear Sir:

This is in response to the Office Action mailed January 4, 2006. Applicant respectfully requests the Examiner to enter the following amendments and consider the following remarks.